

ABSTRACT OF THE INVENTION

- [37] The addition of an array of transistors through areas of the circuit where active devices normally don't exist, such as under routing channels. By connecting this array of transistors such that the gates are tied to one power supply and the
- 5 sources and drains to another, the transistors act as bypass capacitors between the power supplies and act to reduce noise on the supplies. Also, the transistors may later be reconnected through changes in the design to form diodes, inverters, buffers, or other logic gates to allow changes to the circuit late in the design cycle.

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